

1/4

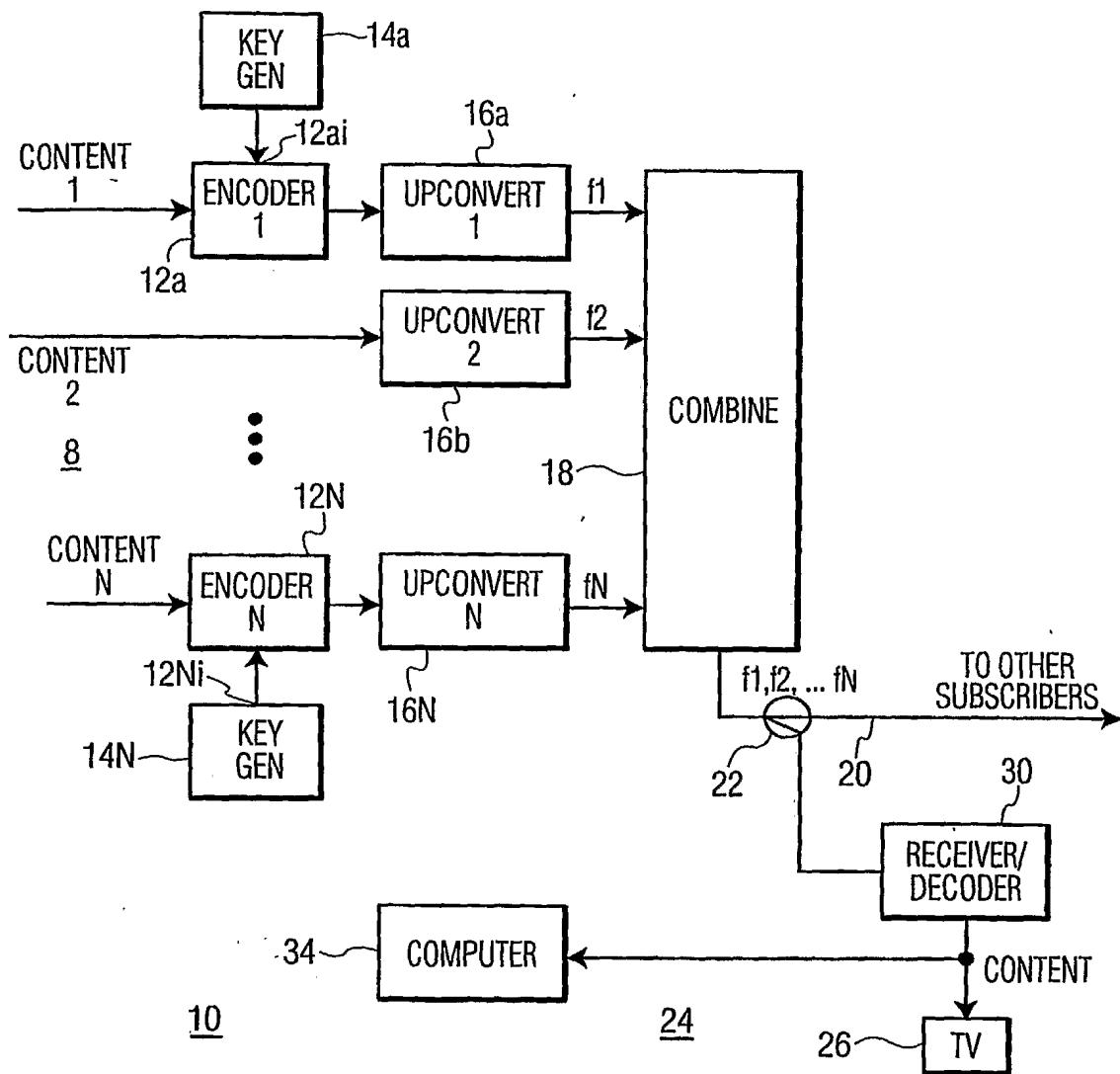


FIG. 1

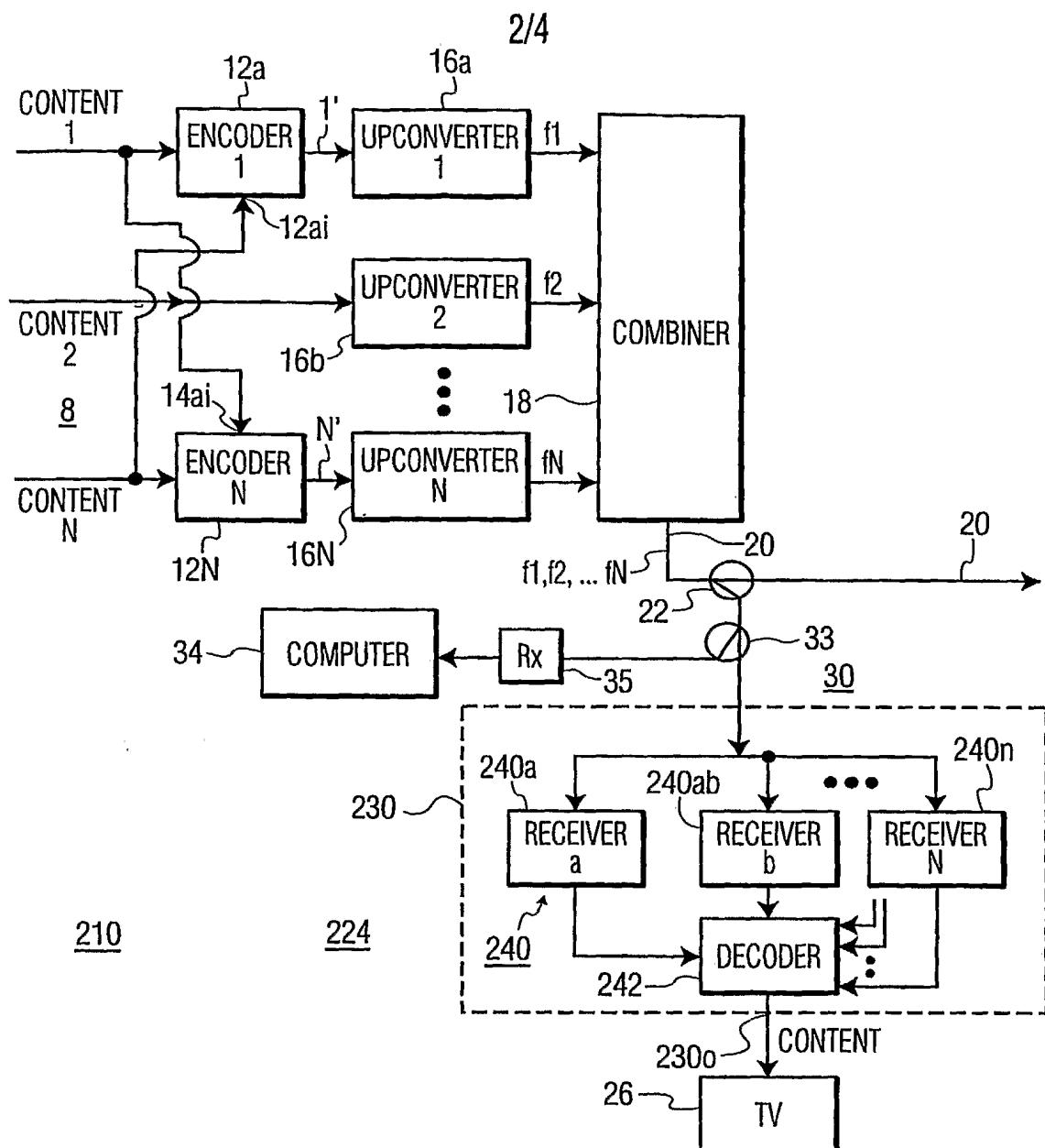


FIG. 2

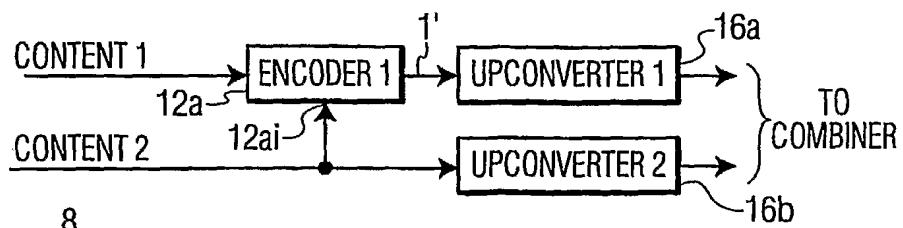


FIG. 3

3/4

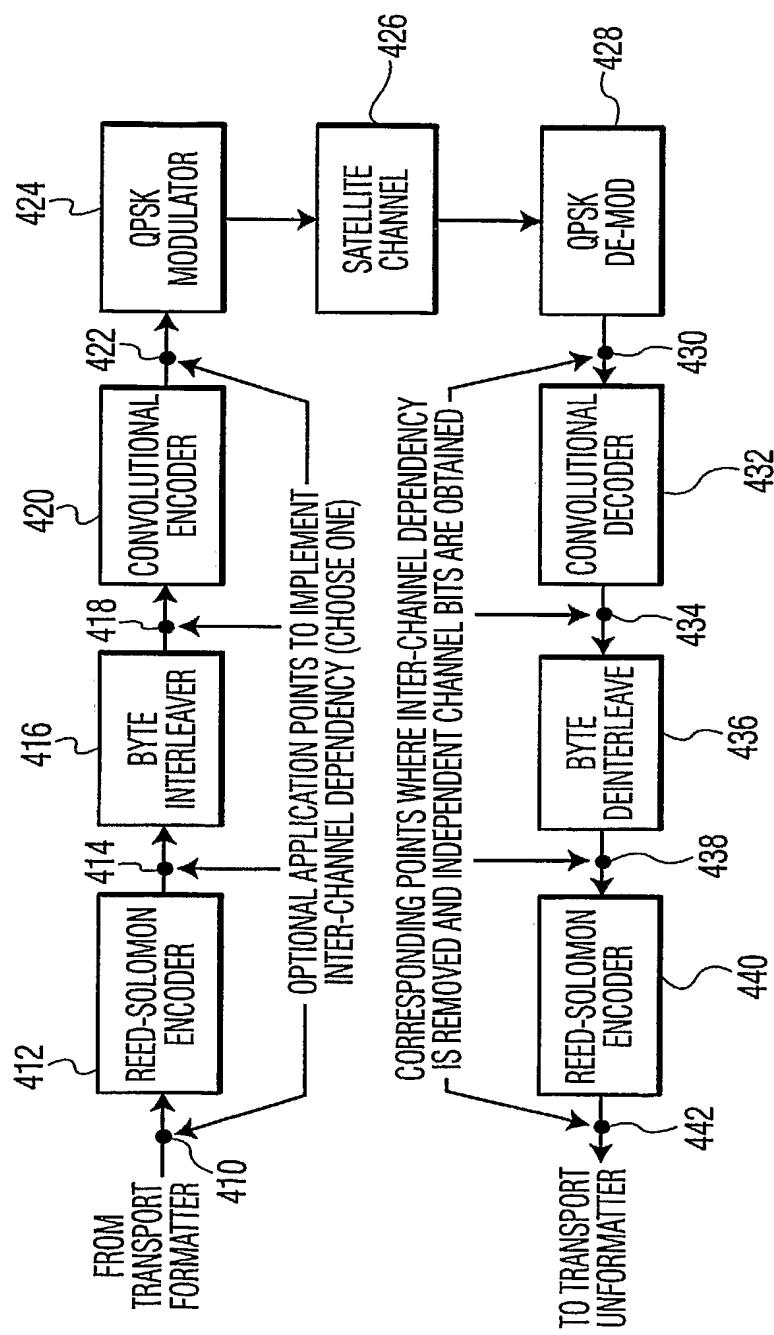
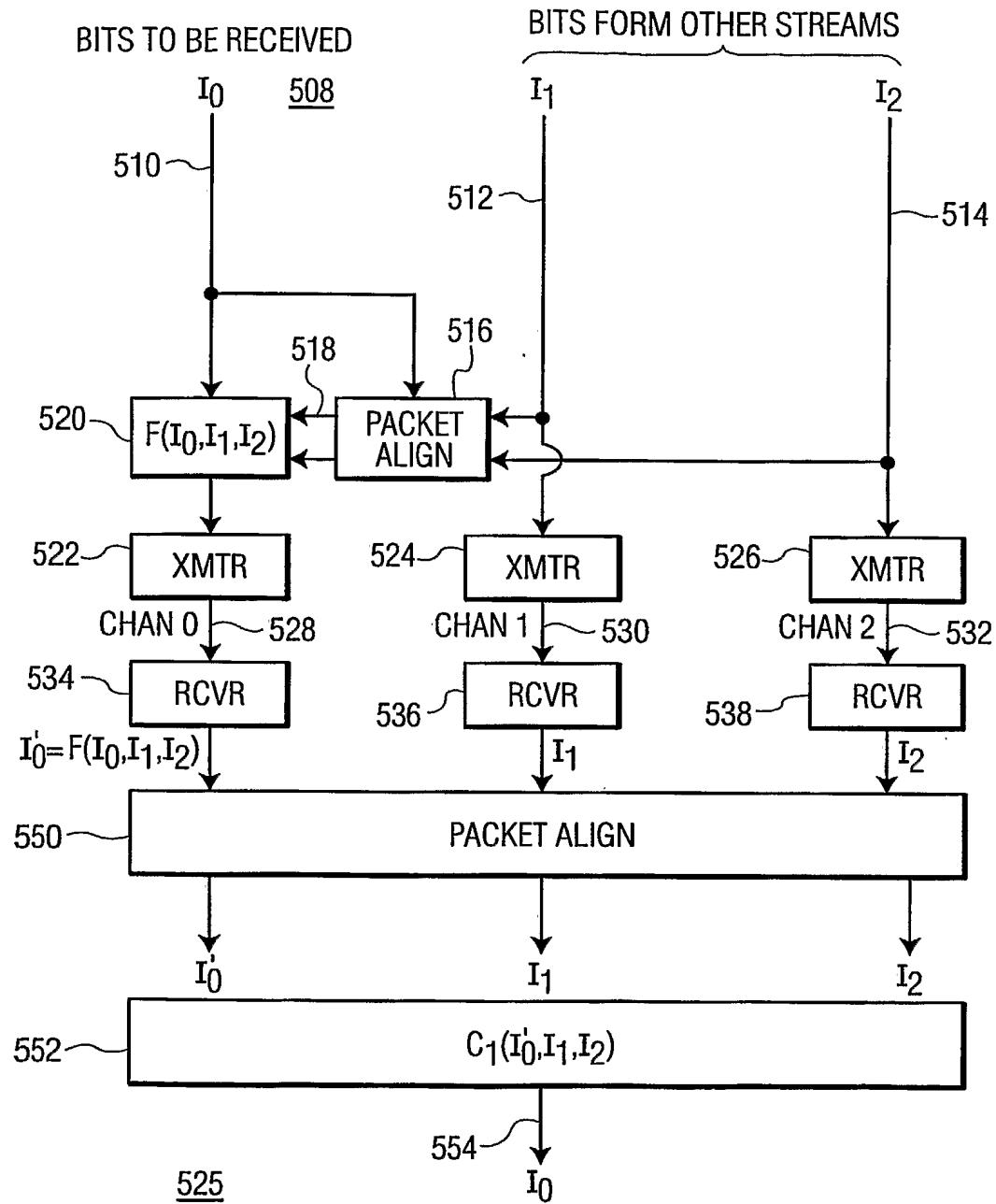


FIG. 4

4/4



NOTE: I₀, I₁, I₂ MAY BE CONTENT
BITS OR WHOLE OR PARTIALLY
ERROR CORRECTED BITS

FIG. 5